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A NOVEL CASCADEDTWO-LEVELH-BRIDGEVOLTAGESOURCEINVERTERBASED STATCOM FOR HIGH POWER APPLICATIONS

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Abstract:

Inthisprojectpresentsavarcompensationbyacascadedtwo-levelH-BridgeVSIbasedmultilevelstatic compensator (STATCOM) usingSVPWM. Thetopology consists oftvo voltage source inverters areconnected in cascaded through a 3-phase transformer. The benefit of this topology is that by maintaining asymmetric voltages at the dccapacitors ofthe inverters, the levels in the waveformof output voltage can be increases. This results power quality (PQ) improved. The main object of this project is balancing the dc link capacitor voltages ofmultilevelinvertersduringbalancingandunbalancingconditions.Thiscontrolleriscontrollinginvertervoltage in such a way that either -vesequence current flowing into the inverteris eliminated or reduces the unbalancing in the grid voltages. The performance of the control scheme during balanced and unbalanced conditions is analysed through MATLAB/SIMULINK.

Keywords: Dc link capacitor voltage balance, Power Quality (PQ), Multilevel voltage source inverters (VSI), Static Compensator, (STATCOM), Volt-Ampere Reactive (VAR), space vector pulse width modulation (SVPWM).

I. INTRODUCTION

In Electrical power system the generation,transmission and distribution of power is a difficult process, it requires working of many components in the power system to produce maximize output. One of the most importantpartsisthereactivepowerinthesystem.Inordertodelivertheactivepowerthroughtransmissionlines it is required to be maintaining the voltage. The reactive power is required for the operation of inductive loads and other loads like motor loads. The efficient management of reactive power in the power system leads to improve the performance of the electrical power system network.

Theefficientcontrolofreactivepowerinthepowersystemisalsoknownasreactivepowercompensation. Theproblemofreactivepowercompensationisassociatedwith:voltagesupportandloadcompensation.Voltage support comprises of

decrease in voltage fluctuation at a given transmission line terminals. Similarly Load compensation comprises of improvement in voltage regulation, power factor, real power balanced drawn from the supply system, etc. of large inconsistent or fluctuating loads. Generally two types of compensation techniques have been used: shunt compensation and series compensation. These compensation techniques change the parameters of the power system and provide better VAR compensation.

In modern years, STATCOM formally known as static VAR compensators are developed. These compensators either generates or absorbs the reactive power even with a fast time response and which are come under Flexible AC Transmission Systems (FACTS). This creates an increased apparent power transfer through a transmission line, and considerably enhanced stability by changing the parameters such as voltage, current phase angle, impedance and frequency that govern the power system.

OBJECTIVES

1. Reactive power compensation by using multi-level STATCOM in deregulated power system especially in high power applications.
2. Reduce the total harmonic distortion (THD) in the output voltage. So that the power quality is improved.

II. METHODOLOGY

1. Determining the need of reactive power compensation
2. Studying different compensation techniques
 - Shunt compensation
 - Series compensation

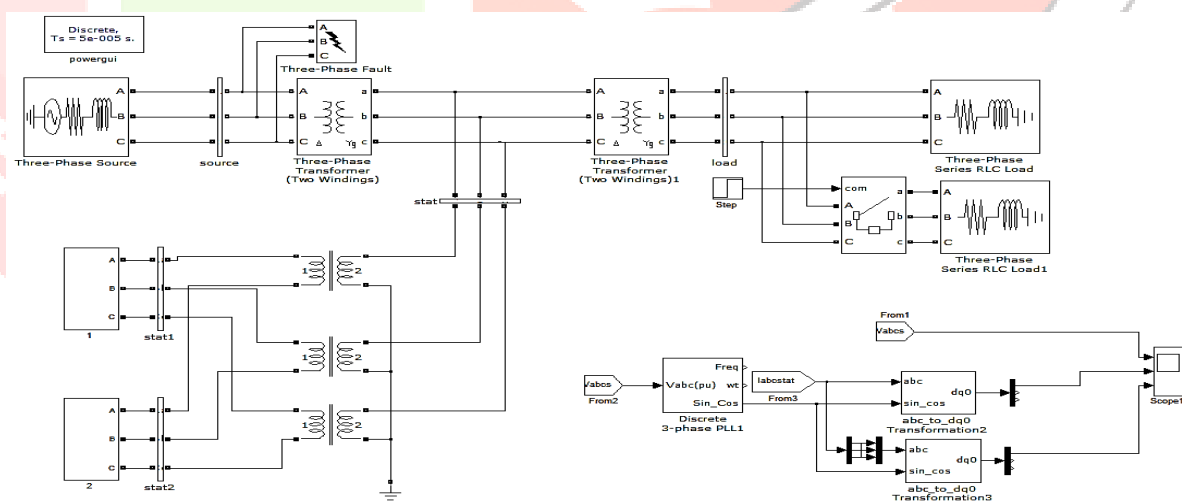
3. Modelling of STATCOM

4. Correction of voltage sag by STATCOM

5. Design of VSI based STATCOM RESULTS AND DISCUSSION

SIMULINK MODEL OF CASCADE H-BRIDGE INVERTER BASED STATCOM

The complete simulation diagram of cascade H-bridge inverter based multi-level STATCOM using MATLAB/Simulink software is shown in and the control circuit is shown in



Simulation diagram of control circuit

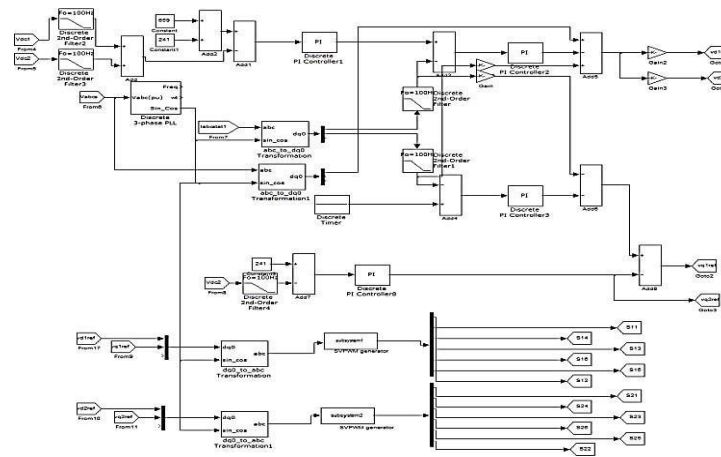


Fig.1.1 Simulink model of control circuit

A. REACTIVE POWER CONTROL

Fig.1.2 shows the waveforms of source voltage and inverter current, DC-link voltage of two inverters in the reactive power control case. In this case reactive power is controlled by setting i_q^* i.e reference reactive current component at a particular reference value. Initially i_q is set at 0.5 p.u. At $t=2.0$ s, i_q^* is changed from 0.5 to -0.5. DC-link voltage of two inverters are regulated during the STATCOM modes are changed.

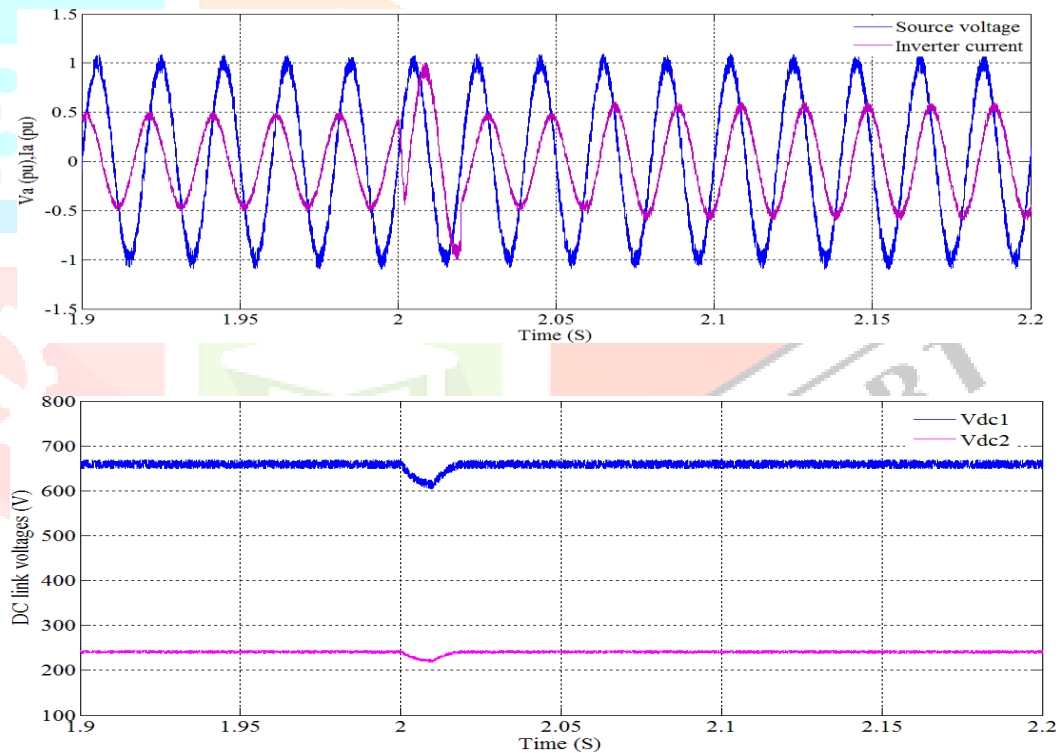
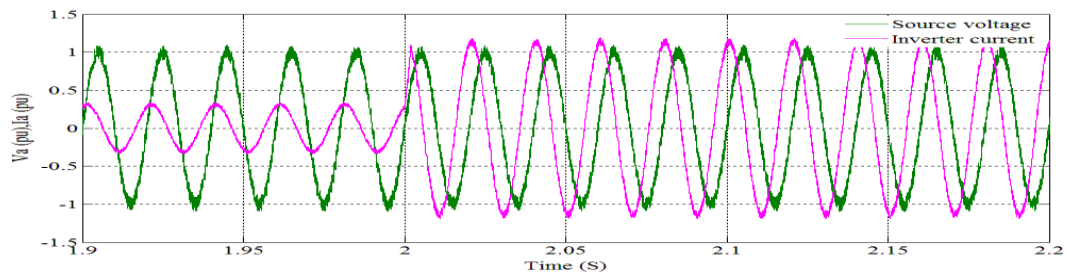


Fig.1.2 Reactive power control waveforms. (a) Source voltage and Inverter current (b) DC-link voltages of two inverters

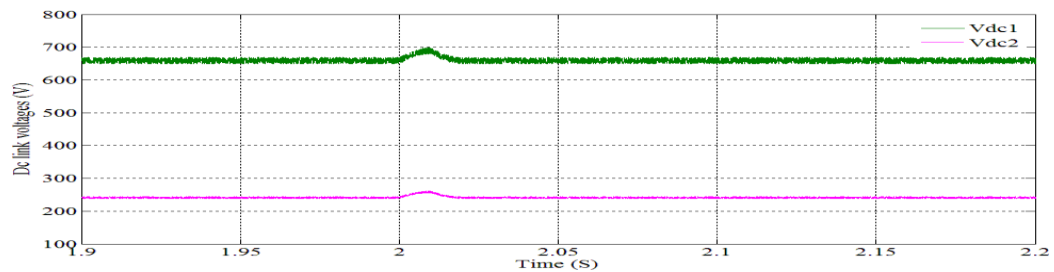
B. LOAD COMPENSATION

Fig. 1.3 shows the waveforms of source voltage and inverter current, DC-link voltage of two inverters in the load compensation case. In this case, reactive power of the load is compensated by the STATCOM. Initially STATCOM supplies the current of +0.5 p.u. When load current increases at $t=2.0$ s, STATCOM supplies more than +0.5 p.u. therefore load compensation is effectively achieved by the STATCOM.

The DC-link voltages of two inverters V_{dc1} and V_{dc2} are regulated at their respective values when STATCOM operating modes are changed.



(a)

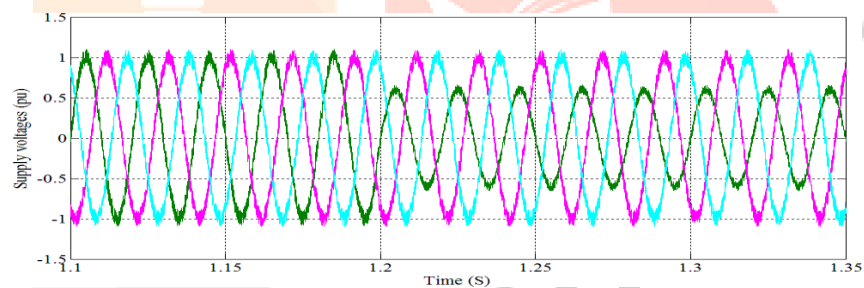


(b)

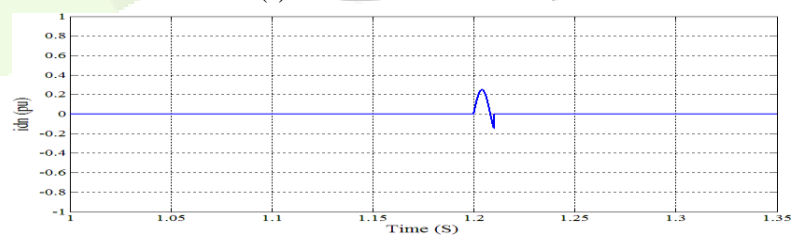
Fig.1.3 Load compensation (a) Source voltage and inverter current
(b) DC-link voltages of two inverters

C. OPERATING DURING THE FAULT CONDITION

Fig.1.4 shows the waveforms of grid voltages on LV side of the transformer, during the fault condition. In which, a single line to ground fault is created at 1.2s and cleared after 200 ms on A phase of HV side of the 33/11 kv transformer. The corresponding d-q axis currents of the inverter are shown. The fault currents are regulated at their respective values i.e at zero.



(a)



(c)

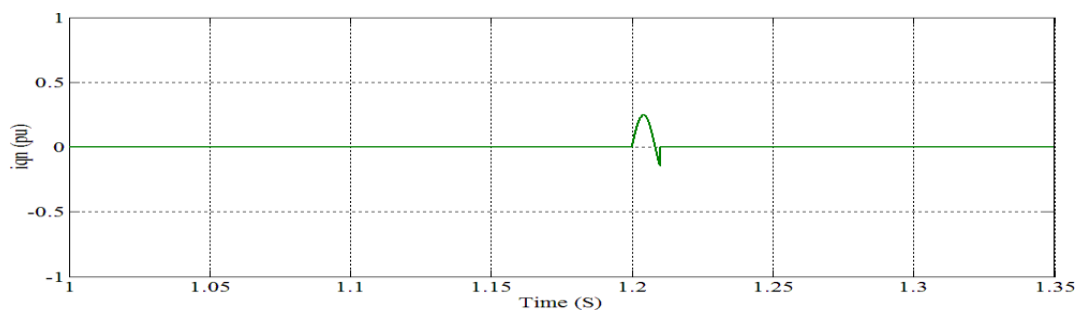
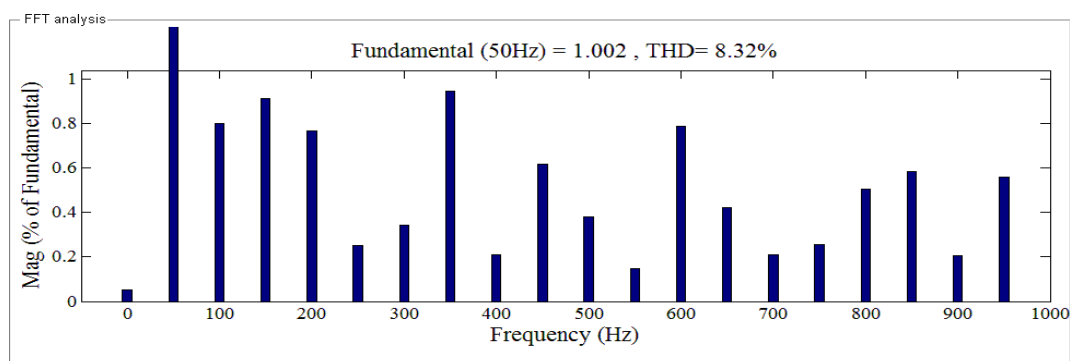


Fig.1.4 Operation during the fault condition (a) Grid voltages on the LV side of the transformer (b) d-axis negative sequence current component (c) q-axis negative sequence current component

FFT ANALYSIS

The frequency spectrum of different signals is obtained by this FFT analysis. Powergui block can be used to obtain the frequency spectrum of any signal directly. The bar graph shows the order of harmonics and its magnitude.

FFT analysis is carried out to study the harmonics spectrum of load voltage



voltage and load current before and after using SVPWM. By using SVPWM the THD of load voltage is reduced from 8.32% to 1.69%. The

THD of load voltage with SPWM and SVPWM is shown in figure.

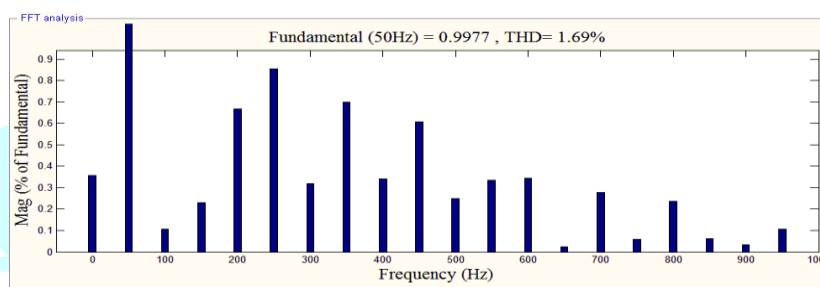


Fig.1.5 THD of load voltage using SVPWM technique

Inverter modulation technique	THD in Load voltage(%)
Sinusoidal Pulsewidth modulation	8.32
Space Vector Pulsewidth modulation	1.69

Table 1.5 Comparison of THD with SPWM and SVPWM

The Total Harmonic Distortion (THD) of load voltage by using both SPWM and SVPWM are tabulated. The harmonics are reduced in the load voltage by using a SVPWM inverter.

IV CONCLUSION

The balancing of dc-link capacitor voltage of the cascaded H-Bridge multilevel inverter based STATCOM is a major issue. In this paper, simple voltage compensation using a cascaded two-level H-Bridge voltage source inverter based multilevel static compensator (STATCOM) is proposed. The scheme ensures regulation of dc-link voltages of inverter at asymmetrical levels and reactive power compensation. The main object of this paper is balancing the dc-link voltages of multilevel inverters during balancing and unbalancing conditions. This controller is controlling inverter voltage in such a way that either the sequence current flowing into the inverter is eliminated or reduces the unbalancing in the grid voltages. SVPWM technique reduces the THD value of load voltage from 8.32% to 1.69%. Thus, power quality is improved with voltage compensation. The performance of the control scheme during balanced and unbalanced conditions is analysed through MATLAB/SIMULINK.

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